TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD AND APPARATUS FOR INITIALIZING MULTIPLE PROCESSORS RESIDING IN AN INTEGRATED CIRCUIT

Application Number :

Date :

First Named Applicant: Robert J. Devins

Confirmation Number:

Attorney Docket Number: BUR920030020US1

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Documents being submitted:	Files
us-fee-sheet	BUR920030020US1-usfees.xml
	us-fee-sheet.xsl
	us-fee-sheet.dtd
us-request	BUR920030020US1-usrequ.xml
	us-request.dtd
	us-request.xsl
us-assignment	BUR920030020US1-usassn.xml
	us-assignment.xsl
	us-assignment.dtd
	BUR920030020US1Assign1of2.tif
	BUR920030020US1Assign2of2.tif
us-declaration	BUR920030020US1Dec1of2.tif
application-body	BUR920030020US1specABX.xml
	application-body.dtd
	Image1.tif
	Image2.tif
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isobox.ent
	isocyr1.ent
	isocyr2.ent
	isodia.ent
	isogrk1.ent
	isogrk2.ent
	isogrk3.ent
	isogrk4.ent
	isolat1.ent
	isolat2.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isonum.ent
	isopub.ent
	isotech.ent
	mathml2.dtd
	mathml2-qname-1.mod
	mmlalias.ent
	mmlextra.ent
	soextblx.dtd
	us-application-body.xsl
	wipo.ent
application-body-pdf-wrap	BUR920030020US1specABX-pdf-wrap.xml
abstract-pdf	BUR920030020US1specABX-abst.pdf
claims-pdf	BUR920030020US1specABX-clms.pdf
description-pdf	BUR920030020US1specABX-desc.pdf

drawings-pdf	BUR920030020US1specABX-draw.pdf
us-declaration	BUR920030020US1Dec2of2.tif

Comments